FIG. 1 PRIOR ART

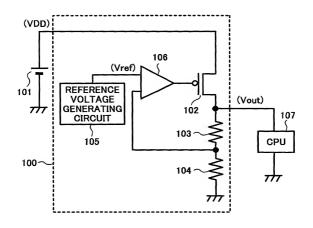


FIG. 2 PRIOR ART

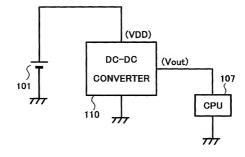


FIG. 3

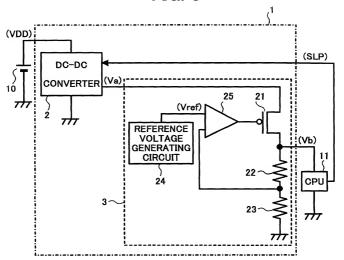


FIG. 4

VDD

31

CONTROLLER

(SLP)

46

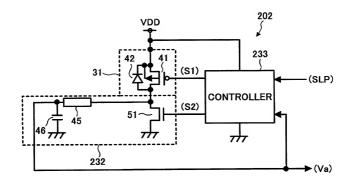
777

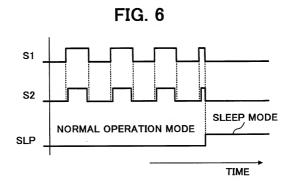
47

32

(Va)

FIG. 5





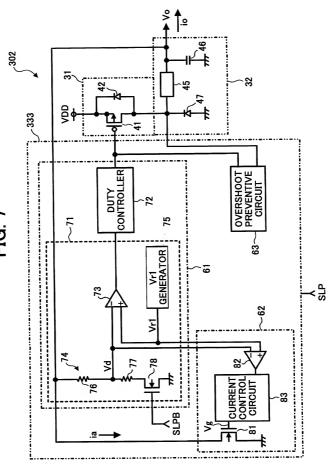


FIG. 7

FIG. 8

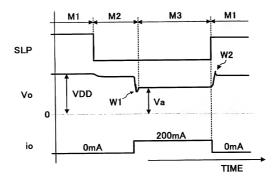


FIG. 9

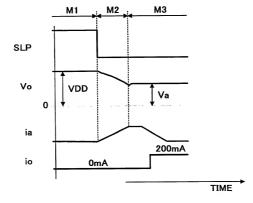


FIG. 10

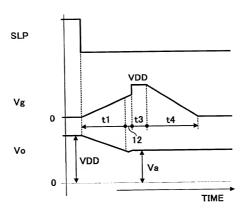


FIG. 11

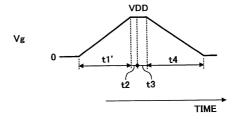
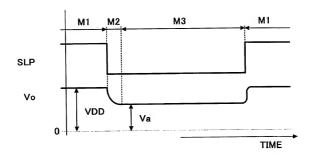
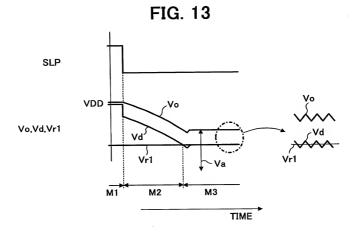


FIG. 12





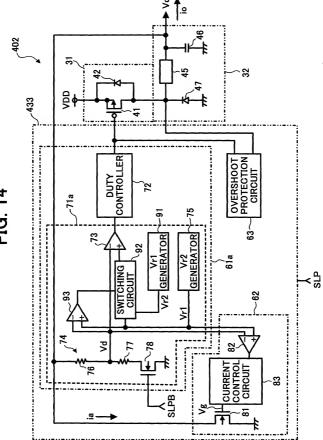


FIG. 14

FIG. 15

